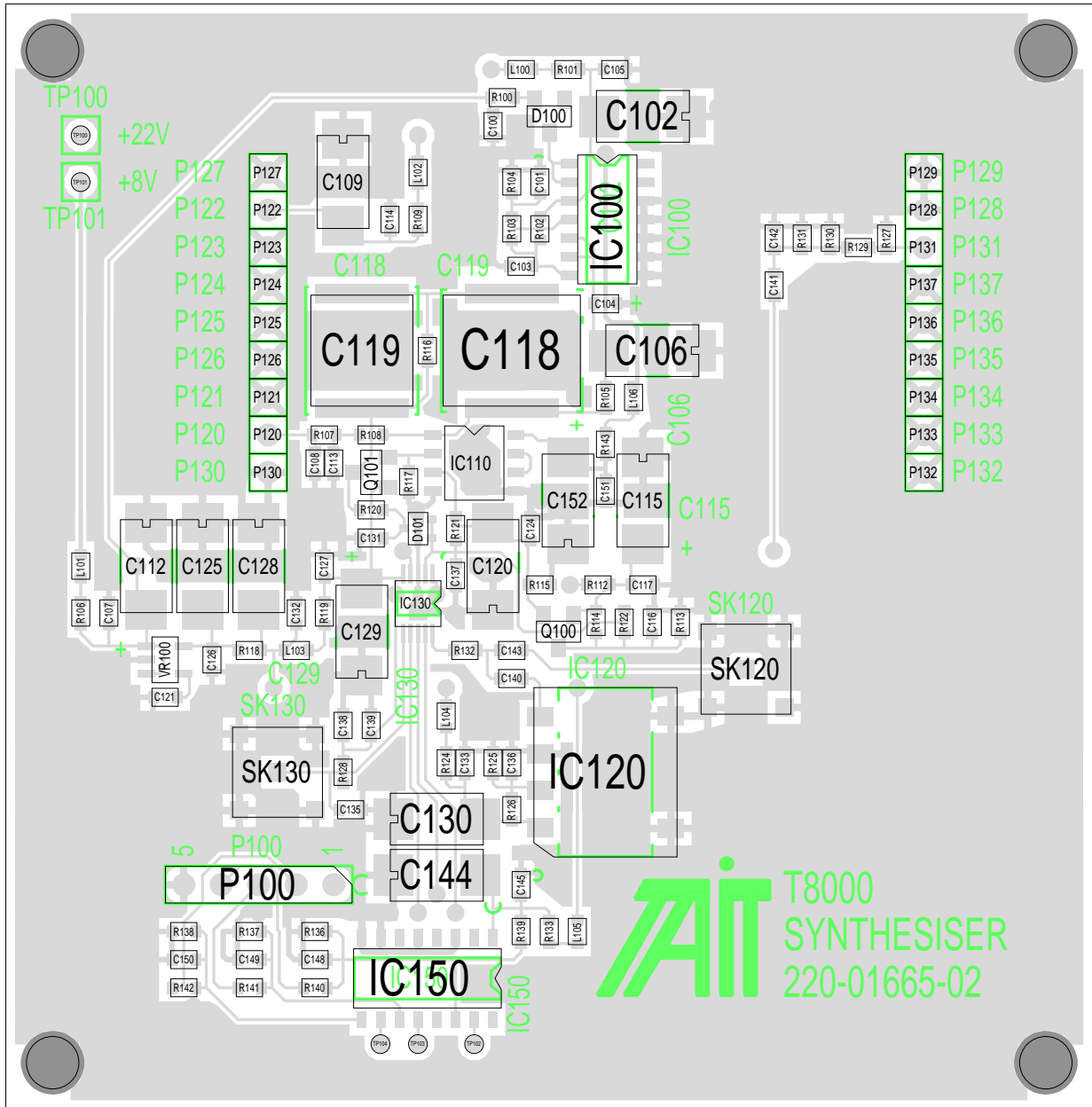


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T8000 SYNTHESISER	
IPN: 220-01665-02	ISSUE: A 2.S.C. 1
PROJECT: T8000	DESIGNER: AA
FILE NAME: 1665_02a	FILE DATE: 20-Oct-00
NO. SHEETS: 1	

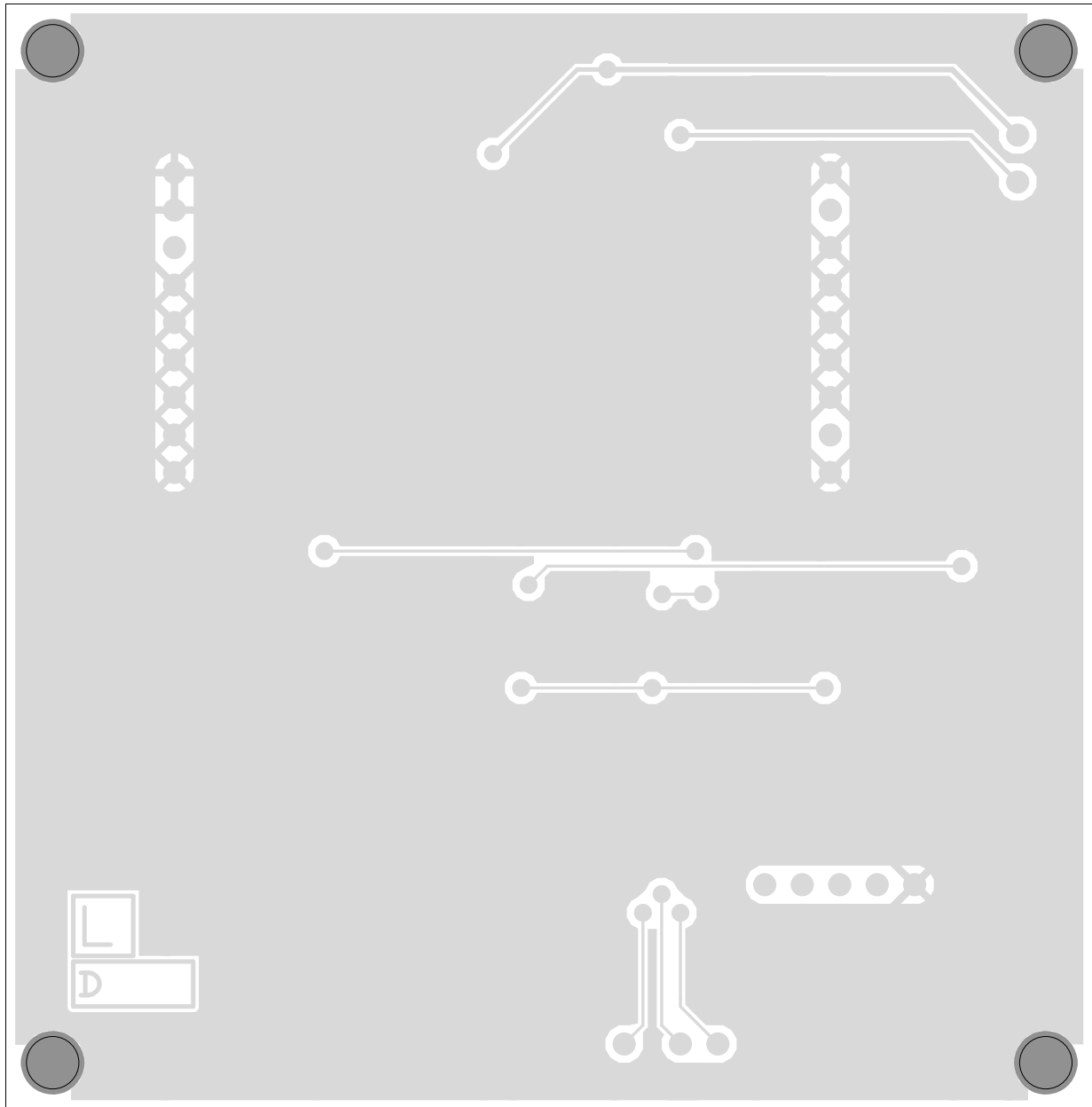
REV/ISS	PROTOTYPE	BENC	CHKD	D.O.	APVD	DATE
	AMENDMENTS	DRAWN				




**T8000**  
**SYNTHESIZER**  
 220-01665-02

<b>TAIT ELECTRONICS</b>	IPN:	ISS:	ID:	DATE:
	220-01665-02	02A	1.TA	20 Oct 2000
<b>SYNTHESIZER PCB LAYOUT - TOP SIDE</b>				

Scale:2.2:1 ; Rotation: 0 degrees



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IPN: 220-01665-02

ISS: 02A

ID: 2.BA

DATE: 20 Oct 2000

SYNTHESISER PCB LAYOUT - BOTTOM SIDE